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Amendments To The Abstract:

In the English translation document, please add the section heading at page 19 line 1, as follows:

--ABSTRACT

The invention relates to a circuit and associated method for synchronizing clock pulses, which enables, with a combined pulse spacing coding and pulse width coding, a simultaneous, collision-free, real time transmission of a number of plesiochronous reference clock signals on a bus line between network units, whereby the selection of the redundant reference clock pulses ensues without involving a central control unit that controls the first and second network unit.--